

In the Specification:

Please replace the paragraph on page 4 lines 1-3, with the following, amended paragraph:

It is yet [[an]]another object of the present invention to provide a means, when necessary, to lock the embedded process time epochs to the simulation engines.

Please replace the paragraph on page 5 lines 13-15 with the following, amended paragraph:

FIG. 2 is a data flow diagram of [[of]]a real-time spacecraft simulation system in accordance with a preferred embodiment of the present invention.

Please replace the paragraph on page 8 line 15-25 with the following, amended paragraph:

The ESCP 40 has a master counter 39, including a master counter count, and a real-time clock 41 that are derived from an on-board oscillator. The real-time clock, in turn, is used to create a local clock[[42]] 42. The master counter 39 master counter count is accessible to the simulation engines 12,13 and is used by them to gauge the bias and drift of the ESCP 40 oscillator and therefore, the real time clock and local clock. The real time clock is an electronic signal on the ESCP 40 that is used to synchronize and moderate time on the ESCP 40. The local clock 42 is derived from the real time clock and is the satellite local calendar time that is telemetered to the ground status and control system. As will be described below, the ESCP 40 local clock, the simulation engine 12,13 clocks and the operating system clock on the VIM 16 are all subject to drift and biasing which may in turn generate inaccurate data.